



Bite-sized technology topics

TEC BYTES
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TRS-STAR

Webinar Invitation

Easy implementation of Hardware / Software Interfaces with HxS

Thursday, 28. 11. 2024 [10 a.m. to 11:00 a.m.] – online

Elevate your development projects to the next level with our latest innovation, HxS — where hardware meets software.

HxS is a game-changer in electronic design automation, meticulously crafted to simplify the creation and management of hardware-software interfaces.

Whether you're an FPGA developer or an embedded software engineer, HxS is your ultimate solution for achieving seamless integration and exceptional productivity.

Speaker:	Bernd Röckert [Founder Eccelerators] Andreas Schwarztrauber [CEO TRS-STAR]
Language:	English
Prerequisites:	Technical understanding
Seminar form:	Presentation, slides which can be downloaded afterwards
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Agenda [Timezone: CET]

Welcome and introduction

Eccelerators

What's a Hardware / Software Interface?

HxS

Clock domain crossing and documentation

Add IP into FPGA Design

Questions and answers

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