



## Efinix® – disruptive FPGA innovation now available on the TRS-STAR Webshop!

Dear <<Anrede-EN>> <<Nachname>>>,

TRS-STAR recently released a [webshop](#) for FPGA-Prototyping. It contains all relevant Efinix Eval Boards and the subset of FPGAs that is relevant for prototyping.

The webshop contains an „Efinix Prototyping Selector Guide“ to help you choose the appropriate prototyping device.



[Trion Selector Guide](#)



[Titanium Selector Guide](#)

### Replace your old CPLD with a modern FPGA



The T13/T20Q100F3 SiP (System in Package) is now in full production. The SiP contains non-volatile memory (16 Mbit SPI-Flash), so the Power-Up behaviour is similar to a CPLD. The 100 pin LQFP 14 mm x 14 mm package with 0,5 mm pitch also reminds of a CPLD, but at heart of the SiP is a modern Trion® T13 or T20 FPGA. Of course a CPLD is instant-on, whereas an FPGA has short configuration-time. E.g. a T13/20 has a typical configuration time of a few ms, but for many applications this is acceptable.

The TRS-STAR Webshop offers the [T20Q100F3I4](#) as prototyping device.

Send us a message to [fpga@trs-star.com](mailto:fpga@trs-star.com), to get an attractive price for production or if you need help converting your old CPLD to a modern FPGA. We are happy to assist.

### New Ti180 packaging variants

The [Ti180](#) is now available in many packaging variants (J361, G400, J484, G529). Scaling from 13 mm x 13 mm to 19 mm x 19 mm.

G400 and J484 are available in **AEC-Q 100 Grade 2**.

The [TRS-STAR Webshop](#) offers the most popular Ti180J484I4 package, but all other variants can be ordered with short lead times.

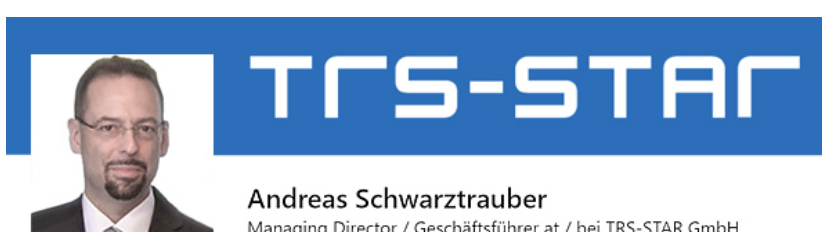
Size BxTxH [mm]	HVIO	HSIO	MIPI	DDR4/4x [Gbps]
<b>J361   Smallest Size</b>				
13 x 13 x 0,65	20	110	2 ports	48 (x16)
<b>G400   I/O Rich</b>				
16 x 16 x 0,8 mm	74	200	0	0
<b>J484 &amp; L484   Rich Hardcore IP</b>				
18 x 18 x 0,8 mm	27	116	4 ports	96 (x32)
<b>G529   High Performance</b>				
19 x 19 x 0,8 mm	48	210	0	106.6 (x32)

### Meet the boss

On 1st of October Andreas Schwarztrauber, son of TRS-STAR founder Manfred Schwarztrauber (founder of MSC Vertriebs GmbH, Gleichmann Electronics and other innovative companies), joined TRS-STAR as CEO.

Andreas Schwarztrauber has a long history in FPGA engineering ranging from ASIC design and FPGA-IP development to FPGA design and FPGA design support.

Contact [nachtigal@trs-star.com](mailto:nachtigal@trs-star.com) to setup a Teams meeting with him and talk about disruptive FPGA innovation developed by Efinix® and TRS-STAR capabilities' in supporting Efinix® FPGA design.



**Andreas Schwarztrauber**

Managing Director / Geschäftsführer at / bei TRS-STAR GmbH

### New to Efinix?

Just use our „Quick Start“ to get your Efinix design started today!

#### More information:

Tel.: +49 7249 95222-0 · E-Mail: [info@trs-star.com](mailto:info@trs-star.com) · [www.trs-star.com](http://www.trs-star.com)